

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

PUBLICATION NUMBER : 63062068  
PUBLICATION DATE : 18-03-88

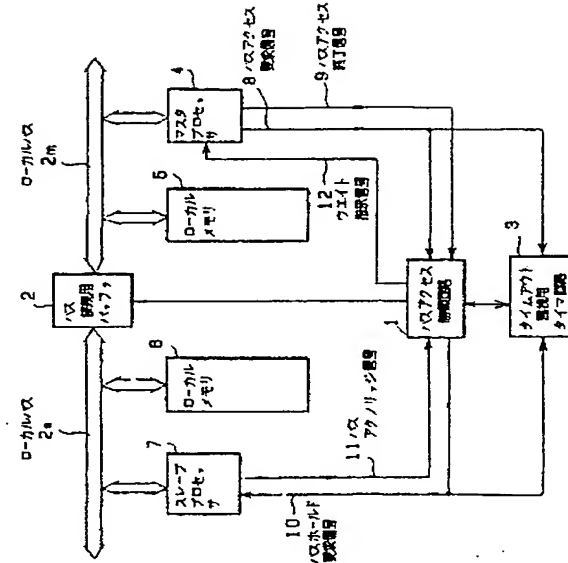
APPLICATION DATE : 03-09-86  
APPLICATION NUMBER : 61208230

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INT.CL. : G06F 15/16 G06F 13/36 G06F 15/16

TITLE : INTER-PROCESSOR INTERFACE CIRCUIT



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ABSTRACT : PURPOSE: To eliminate a complicated control mechanism for semaphore using software by deleting a shared memory and giving direct access to a local memory of each processor to carry out data communication.

CONSTITUTION: A master processor 4 is connected to a local memory 5 via a local bus 2m; while a slave processor 7 is connected to a local memory 6 via a local bus 2s. A bus connecting buffer 2 connects or disconnects the bus 2m of the master side and the bus 2s of the local side via control signals. A bus access control circuit 1 is connected to both processors 4 and 7 respectively as well as to the buffer 2. A time-out monitoring timer circuit 3 is connected to both processors 4 and 7 and also to the circuit 1 to monitor a time-out state.

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